L Number	Hits	Search Text	DB	Time stamp
10	10231	324/761,158.1,537,765,500.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/11/13 13:57
11	6576	test\$4 near5 ((printed adj circuit adj board) or PCB!)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/11/13 13:58
12	758	324/761,158.1,537,765,500.ccls. and (test\$4 near5 ((printed adj circuit adj board) or PCB!))	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/11/13
13	76	(324/761,158.1,537,765,500.ccls. and (test\$4 near5 ((printed adj circuit adj board) or PCB!))) and roll\$4	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/11/13 13:59
14	40	((324/761,158.1,537,765,500.ccls. and (test\$4 near5 ((printed adj circuit adj board) or PCB!))) and roll\$4) and hous\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/11/13
-	2	("6191600").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/11/13 13:57
_	3	("3629702" "4836797" "5506510").PN.	USPAT	2003/10/23
_	6534	test\$4 near5 ((printed adj circuit adj board) or PCB!)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/11/13
_	2192	short\$4 near3 matrix	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/23 17:45
_	5	(test\$4 near5 ((printed adj circuit adj board) or PCB!)) and (short\$4 near3 matrix)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/23 18:06
_	1277	(test\$4 near5 ((printed adj circuit adj board) or PCB!)) and hous\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/10/23 18:07
_	136	((test\$4 near5 ((printed adj circuit adj board) or PCB!)) and hous\$4) and roll\$4	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/10/23 18:07
-	18	("1576163" "1789132" "2164174" "2471330" "2499572" "3045273" "3172780" "3495932" "3749053" "3956790" "4044716" "4102299" "4173945" "4288475" "4736221"	IBM_TDB USPAT	2003/10/23
_	928	"4773110" "4926520" "4968534").PN. 324/761.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 18:51

_	280	(test\$4 near5 ((printed adj circuit adj board) or PCB!)) and 324/761.ccls.	USPAT; US-PGPUB; EPO; JPO;	2003/10/23 18:51
			DERWENT; IBM_TDB	
_	29	((test\$4 near5 ((printed adj circuit adj board) or PCB!)) and 324/761.ccls.) and hous\$4 and roll\$4	USPAT; US-PGPUB; EPO; JPO;	2003/10/23 18:54
	4488	324/158.1.ccls.	DERWENT; IBM_TDB USPAT;	2003/10/23
	4400	3247 130.1. cc15.	US-PGPUB; EPO; JPO; DERWENT;	18:54
-	266	(test\$4 near5 ((printed adj circuit adj board) or PCB!)) and 324/158.1.ccls.	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/10/23 18:54
	2	(((test\$4 near5 ((printed adj circuit adj	DERWENT; IBM_TDB USPAT;	2003/10/23
_	2	board) or PCB!)) and 324/158.1.ccls.) and continui\$4) and hous\$4 and roll\$4	US-PGPUB; EPO; JPO; DERWENT; IBM TDB	18:55
_	42	((test\$4 near5 ((printed adj circuit adj board) or PCB!)) and 324/158.1.ccls.) and continui\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/10/23
-	555	134/64R.ccls.	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/10/23 19:00
-	1	134/64R.ccls. and (test\$4 near5 ((printed adj circuit adj board) or PCB!))	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/23 19:00
-	18	"2471330" "2499572" "3045273" "3172780" "3495932" "3749053" "3956790" "4044716" "4102299" "4173945" "4288475" "4736221"	USPAT	2003/10/23 19:01
	197094	"4773110" "4926520" "4968534").PN. ((printed adj circuit adj board) or PCB!)	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/10/23 19:03
_	28	134/64R.ccls. and (((printed adj circuit adj board) or PCB!))	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/10/23 19:05
-	42	396/609.ccls.	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/10/23 19:05
_	0	(((printed adj circuit adj board) or PCB!)) and 396/609.ccls.	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/10/23 19:05
-	295	216/91.ccls.	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/10/23 19:05
			DERWENT; IBM TDB	

-	12	216/91.ccls. and (((printed adj circuit	USPAT;	2003/10/23
		adj board) or PCB!))	US-PGPUB;	19:07
			EPO; JPO;	
			DERWENT;	
ļ			IBM_TDB	
-	3434	324/765.ccls.	USPAT;	2003/10/23
			US-PGPUB;	19:07
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	/
-	372		USPAT;	2003/10/23
		adj board) or PCB!))	US-PGPUB;	19:07
			EPO; JPO;	
			DERWENT;	
		1,1204/765	IBM_TDB	2002/10/22
-	0	((= = : , . = : . = :	USPAT;	2003/10/23
1		circuit adj board) or PCB!))) and	US-PGPUB;	19:08
ì		continui\$4) and roll\$4 and hous\$4	EPO; JPO;	
			DERWENT;	
		/204/765 3 ///	IBM_TDB	2002/10/22
-	64	(324/765.ccls. and (((printed adj circuit	USPAT;	2003/10/23
		adj board) or PCB!))) and continui\$4	US-PGPUB; EPO; JPO;	19.00
			DERWENT;	
			IBM TDB	
	5	"4049022"	USPAT;	2003/10/30
-	5	"4848933"	US-PGPUB;	14:44
			EPO; JPO;	
	1		DERWENT;	
			IBM TDB	
_	40	"4818933"	USPAT;	2003/10/30
-		4010933	US-PGPUB;	14:45
			EPO; JPO;	13.33
			DERWENT;	
			IBM TDB	
l _	3	"6191600"	USPAT;	2003/11/10
_		0131000	US-PGPUB;	11:17
			EPO; JPO;	== /
			DERWENT;	
			IBM TDB	
_	4970	test\$4 near5 (printed adj circuit adj	USPAT;	2003/11/10
	13,0	board)	US-PGPUB;	10:39
1		2011,	EPO; JPO;	
			DERWENT;	
			IBM TDB	
-	3414	mov\$4 near5 (printed adj circuit adj	USPĀT;	2003/11/10
]		board)	US-PGPUB;	10:18
			EPO; JPO;	
			DERWENT;	
1			IBM_TDB	
-	3869		USPĀT;	2003/11/10
1		board)) and "4"	US-PGPUB;	10:18
			EPO; JPO;	
1			DERWENT;	1
		l.,	IBM_TDB	2002/11/10
-	287		USPAT;	2003/11/10
		board)) and (mov\$4 near5 (printed adj	US-PGPUB;	10:19
		circuit adj board))	EPO; JPO;	
			DERWENT;	
		//togted popular /printed and administration	IBM_TDB	2003/11/10
-	64	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	USPAT; US-PGPUB;	2003/11/10 10:19
		board)) and (mov\$4 near5 (printed adj	EPO; JPO;	10:13
		circuit adj board))) and sensor	DERWENT;	
			IBM TDB	
_	6563	test\$4 near5 ((printed adj circuit adj	USPAT;	2003/11/10
	0303	board) or (PCB!))	US-PGPUB;	10:40
		Double, of (100.)	EPO; JPO;	
			DERWENT;	
			IBM TDB	
L	.1	1	1	

_	294	<pre>(mov\$4 near5 (printed adj circuit adj board)) and (test\$4 near5 ((printed adj circuit adj board) or (PCB!)))</pre>	USPAT; US-PGPUB; EPO; JPO;	2003/11/10
	·	circuit adj board) of (PCB://)	DERWENT; IBM TDB	
-	67	((mov\$4 near5 (printed adj circuit adj board)) and (test\$4 near5 ((printed adj circuit adj board) or (PCB!)))) and	USPĀT; US-PGPUB; EPO; JPO;	2003/11/10 10:40
		sensor	DERWENT; IBM_TDB	
-	3663	<pre>test\$3 near9 (PCB or (printed near1 board))</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/11/10 11:18
_	6599	(test\$3 or detect\$3) near9 (PCB or (printed near1 board))	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/11/10 11:18
		0 (22)	DERWENT; IBM_TDB	2003/11/10
-	332	((test\$3 or detect\$3) near9 (PCB or (printed near1 board))) and rollers	USPAT; US-PGPUB; EPO; JPO; DERWENT;	11:20
-	1335	short\$4 near2 matrix	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/11/10 14:08
-	5	(test\$4 near5 (printed adj circuit adj	DERWENT; IBM_TDB USPAT; US-PGPUB;	2003/11/10 14:10
		board)) and (short\$4 near2 matrix)	EPO; JPO; DERWENT; IBM_TDB	
-	7	(test\$4 near5 ((printed adj circuit adj board) or (PCB!))) and interpos\$4 adj layer	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/11/10 14:11
-	41	(test\$4 near5 ((printed adj circuit adj board) or (PCB!))) and compliant near2 layer	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/11/10 14:27
-	2274	(test\$4 near5 ((printed adj circuit adj board) or (PCB!))) and short\$4	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/11/10 14:26
_	57	(test\$4 near5 ((printed adj circuit adj board) or (PCB!))) and compliant near2 material	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/11/10 14:39
-	436	(test\$4 near5 ((printed adj circuit adj board) or (PCB!))) and elastic	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/11/10 14:41
-	130	((test\$4 near5 ((printed adj circuit adj board) or (PCB!))) and elastic) and 324/\$.ccls.	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/11/10 14:42
		321/4.0013.	DERWENT; IBM TDB	
_	14	((test\$4 near5 ((printed adj circuit adj board) or (PCB!))) and elastic) and 324/765,158.1.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/11/10 14:44
			IBM TDB	

-	4	((test\$4 near5 ((printed adj circuit adj	USPAT;	2003/11/10
		board) or (PCB!))) and compliant near2	US-PGPUB;	15:23
		material) and 324/765,158.1.ccls.	EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	0	compliant near2 between near2 hous\$4 with	USPAT;	2003/11/10
		((printed adj circuit adj board) or	US-PGPUB;	15:24
		PCB!)	EPO; JPO;	
		,	DERWENT;	·
			IBM TDB	
_	30	compliant near6 hous\$4 with ((printed	USPAT;	2003/11/10
		adj circuit adj board) or PCB!)	US-PGPUB;	15:25
	1	, , , , , , , , , , , , , , , , , , , ,	EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	4	(compliant near6 hous\$4 with ((printed	USPAT;	2003/11/10
		adj circuit adj board) or PCB!)) and	US-PGPUB;	15:25
		(test\$4 near5 ((printed adj circuit adj	EPO; JPO;	
		board) or (PCB!)))	DERWENT;	
			IBM TDB	